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35617 7590 12/05/2003

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March 3, 2004
Date

Kevin L. Daffer

APPLICATION NO.	FILING DATE	FIRST NAMES INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/080,036	02/19/2002	Vani Verma	5732-00300	3980

TITLE OF INVENTION: MEMORY MODULE HAVING INTERCONNECTED AND STACKED INTEGRATED CIRCUITS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1,330.00	\$300.00	\$1,630.00	03/05/2004
EXAMINER	ART UNIT	CLASS-SUBCLASS			
Thai, Luan C.	2827	257-777.000			

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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2. For printing on the patent front page, list (1) the names of up to 3 registered patent attorneys or agents OR, alternatively, (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

1 Kevin L. Daffer

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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type). PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. Inclusion of assignee data is only appropriate when an assignment has been previously submitted to the PTO or is being submitted under separate cover. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE: Matrix Semiconductor, Inc.

(B) RESIDENCE (CITY & STATE OR COUNTRY): Santa Clara, CA

Please check the appropriate assignee category indicated below (will not be printed on the patent): ☐ individual ☒ corporation or other private group entity ☐ government

4a. The following fees are enclosed:

☒ Issue Fee

☒ Publication Fee

☒ Advance Order - # of Copies 4

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Kevin L. Daffer
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(Date)

3/3/04

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